

JACQUELINE T. WALKER

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Education

MSc Communications & Signal Processing [exp. 1st] Sep. 2025 - Sep. 2026, relevant modules

Wireless Comm. Sys. Design (ELEC5333M),
High Speed Internet Architecture (ELEC5461M),
DSP for Comms. (ELEC5442M)

BEng Electronic & Computer Engineering [2:1] Sep. 2021 - Jan 2025, relevant modules

Digital Design with HDL (ELE00030I),
Noise, Waves & Fields (ELE00034I),
Mobile Comms. and Internet Protocols (ELE00077H),
Comm. Sys. & Digital Comms. (ELE00078H),
Committee Member for York Engineering Society 2023-24, responsible for lab session support, event advertising, and other tasks as needed

The Grammar School at Leeds Sep. 2014 - Sep. 2021,

A-Levels: Computer Science (A), Physics (B), Mathematics (C)

Project Work

BEng Final-Year Project Individual project for visualisation of mapping tasks & routing on many-core processors, with support for modularity.

MEng Research Project Individual project researching error correction & reconciliation protocols used within quantum-based secure communication.

FM Radio Receiver Design project of a super-heterodyne receiver for listening to commercial FM radio.

4-Port USB Hub Design project of a 4-port USB 2.0 hub for use within a 5.25" PC drive bay.

Relevant Skills

- Document typesetting with \LaTeX
- Circuit simulation with ADS & LTspice
- Knowledge of C & Python
- Proficiency with Linux & Windows
- Exposure to PCB design via KiCad

Interests

- Sewing & crocheting
- Satellite comms. & hobby radio
- Boulderling & climbing
- Cooking & coffee brewing